- a second transimpedance converter (14) having its output connected to said first output (OUT+), and

- a third transimpedance converter (15) having its output connected to said second output (OUT).

A1 cnt

5. (amended) Phase shifter in accordance with claim 3, characterized in that the transimpedance converter (12; 14; 15) is a transimpedance amplifier.

6. (amended) Phase shifter in accordance with claim 2, characterized in that said first and second output buffer means are said second and third transimpedance converters (14, 15), respectively.

7. (amended) Phase shifter in accordance with claim 1, characterized by at least

- a first transistor (T_1) with its collector connected to its base and its emitter coupled to a predetermined potential,

- second transistor (T_2) with its base connected to the base of said first transistor and its emitter coupled to said predetermined fixed potential, and

- a capacitor (C) coupled between the junction of the bases of said first and second transistor (T_1, T_2) and said predetermined potential.

8. (amended) Phase shifter in accordance with claim 1, provided as a differential phase shifter comprising

- a first input (IN+) for inputting an input signal, and
- a second input (IN-) for inputting an inverse input signal,

characterized by at least

- a first transistor with its collector connected to its base and its emitter coupled to a predetermined potential,
- a second transistor with its base connected to the base of said first transistor and its emitter coupled to said predetermined potential,
- a third transistor with its collector connected to its base and its emitter coupled to a predetermined potential,
- a fourth transistor with its base connected to the base of said third transistor and its collector coupled to said predetermined potential, and